

FIG.1

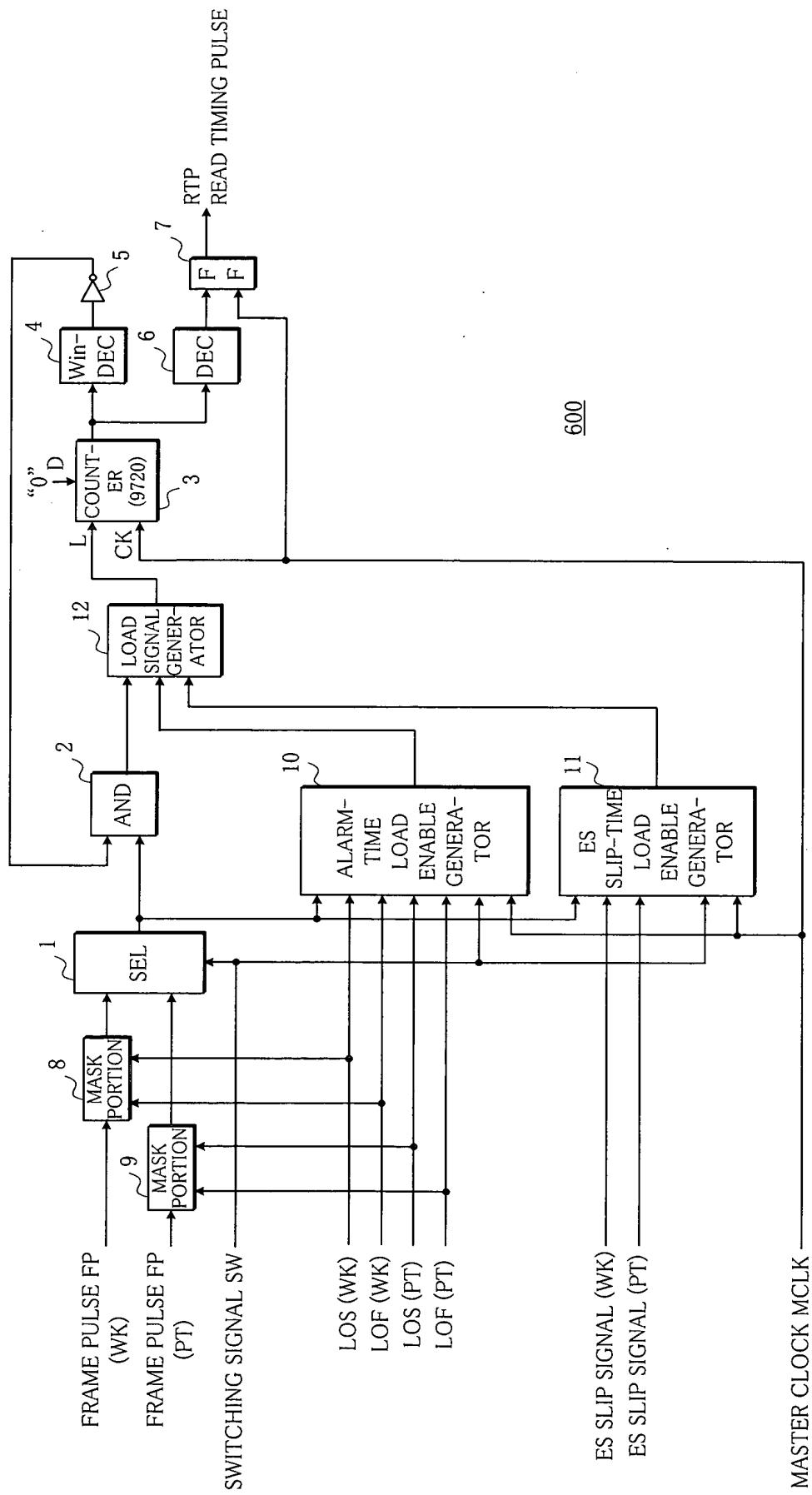
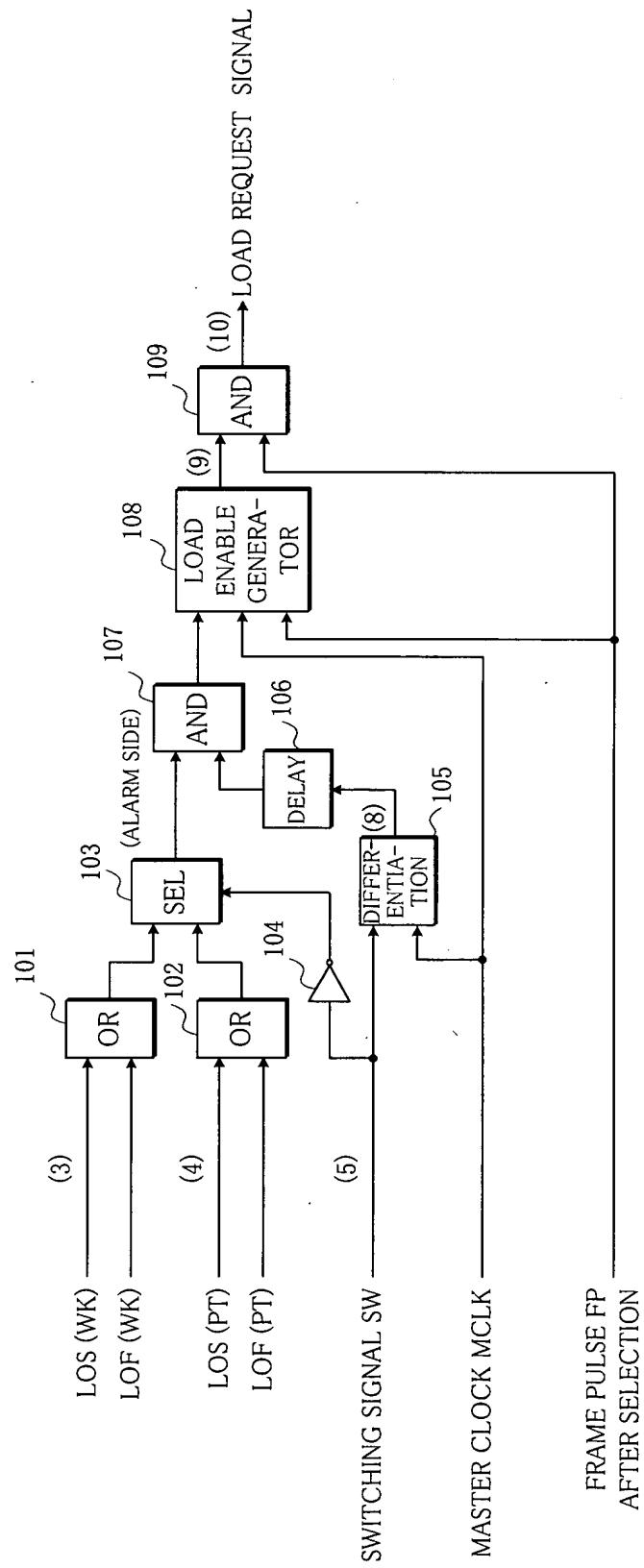


FIG.2



FRAME PULSE FP
AFTER SELECTION

FIG.3A

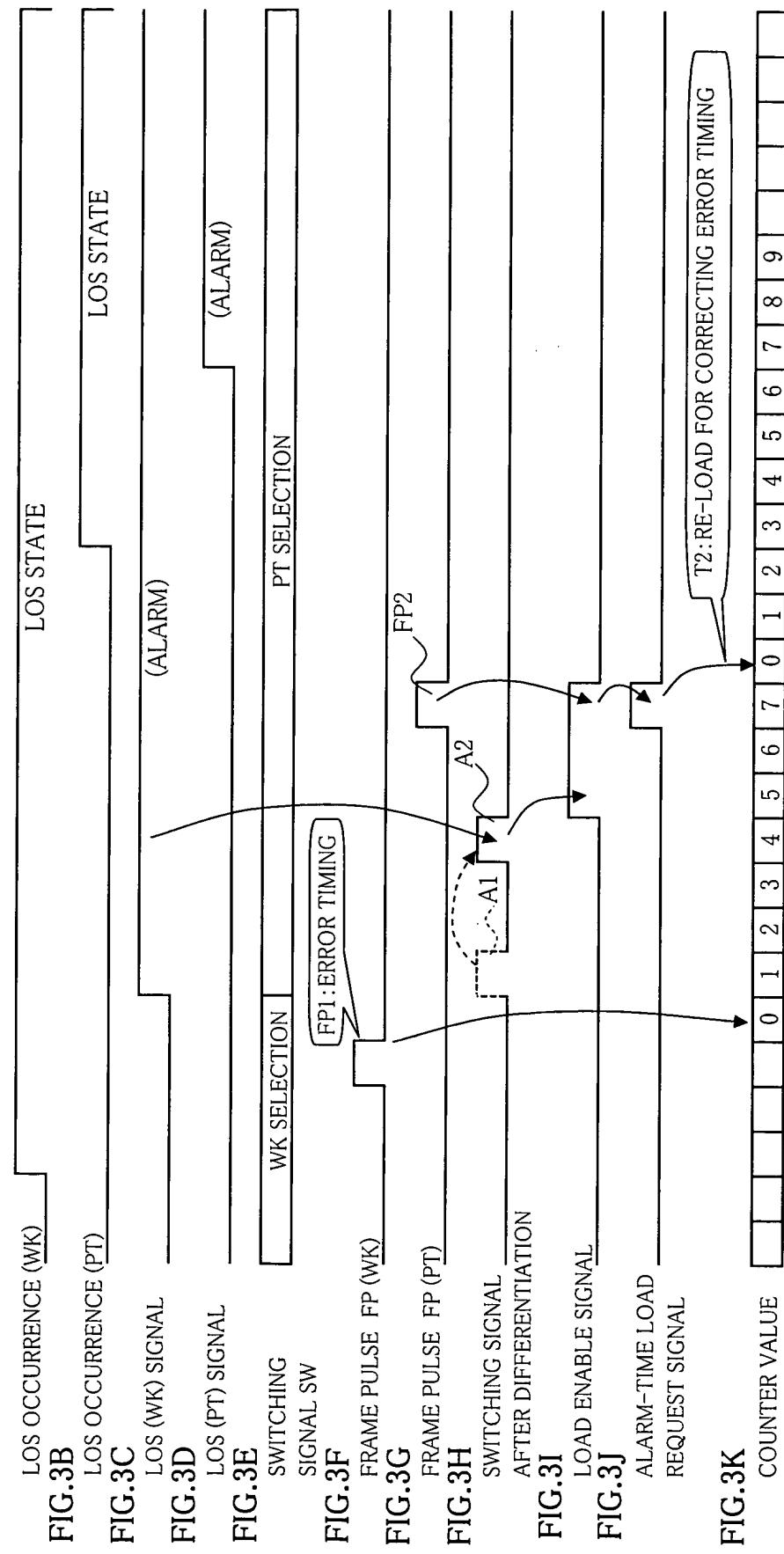


FIG.4

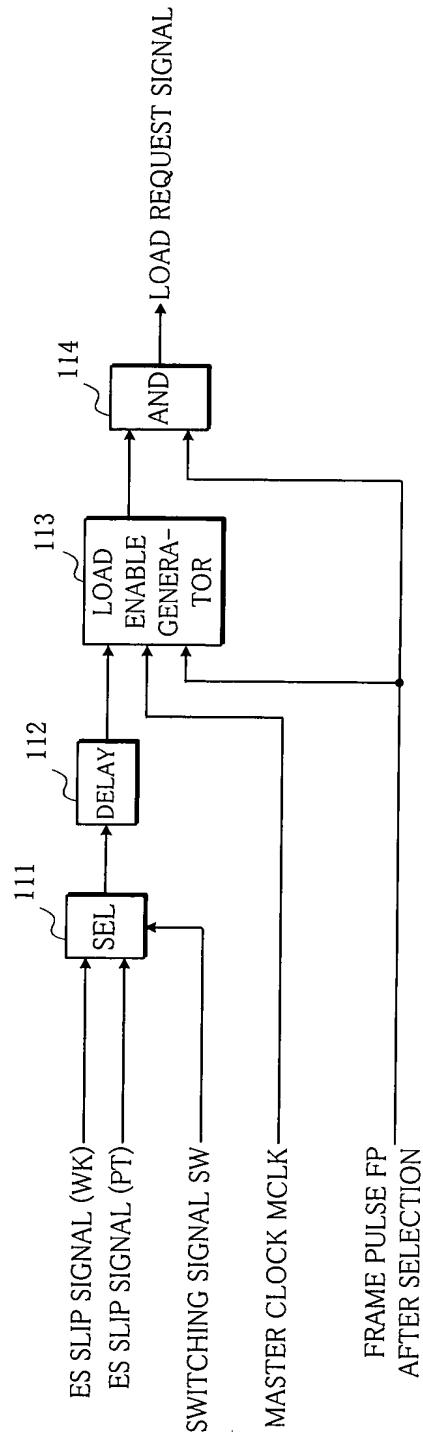


FIG.5

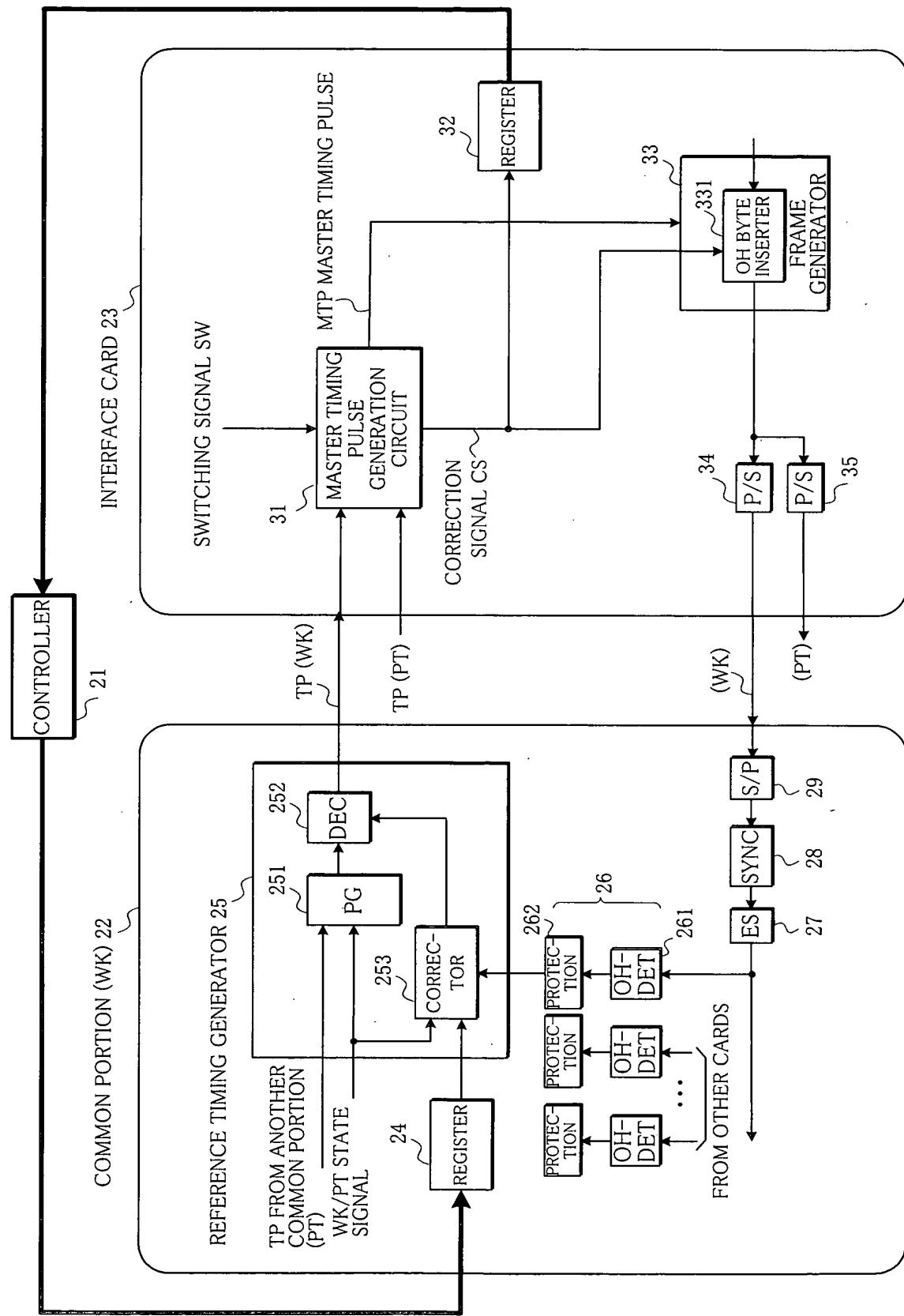


FIG.6

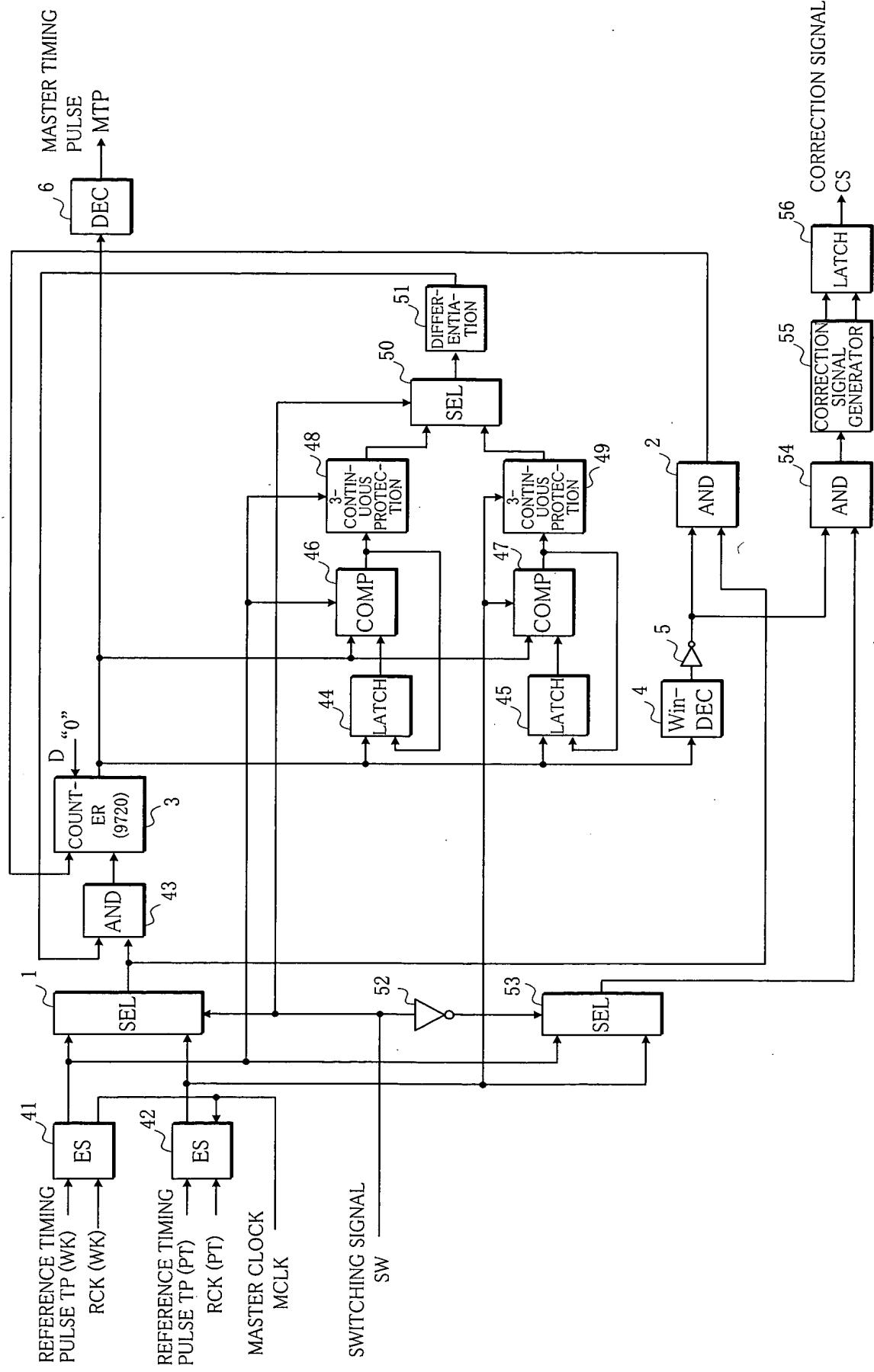


FIG.7

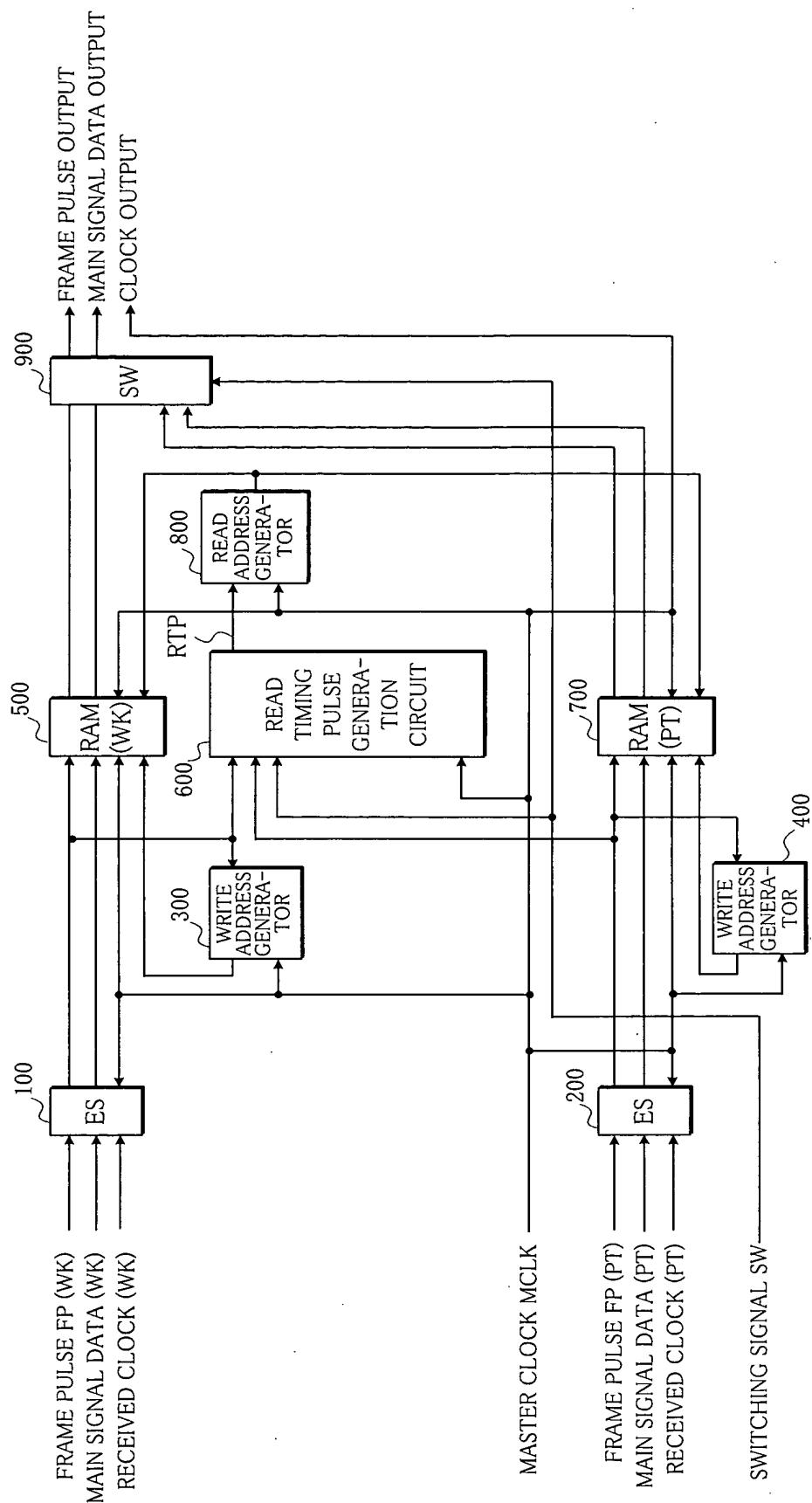
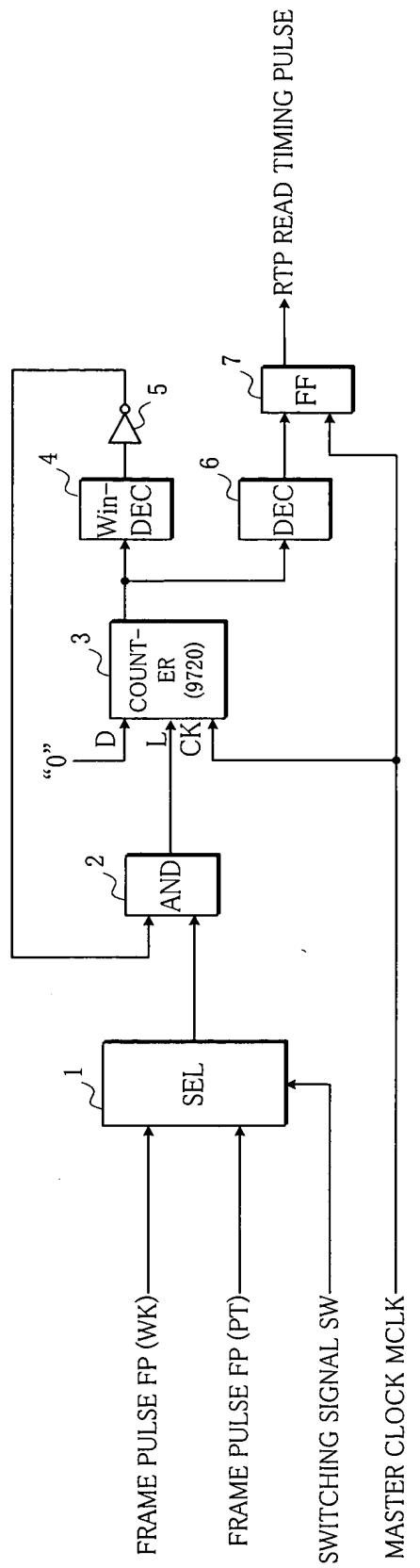


FIG.8
PRIOR ART



PRIOR ART

FIG.9A

POWER-ON RESET

FIG.9B

FRAME PULSE FP (WK)

FIG.9C

FRAME PULSE FP (PT)

FIG.9DFRAME PULSE FP
AFTER SELECTION**FIG.9E**

COUNTER VALUE

FIG.9F

Win-DEC OUTPUT

FIG.9G

DEC OUTPUT

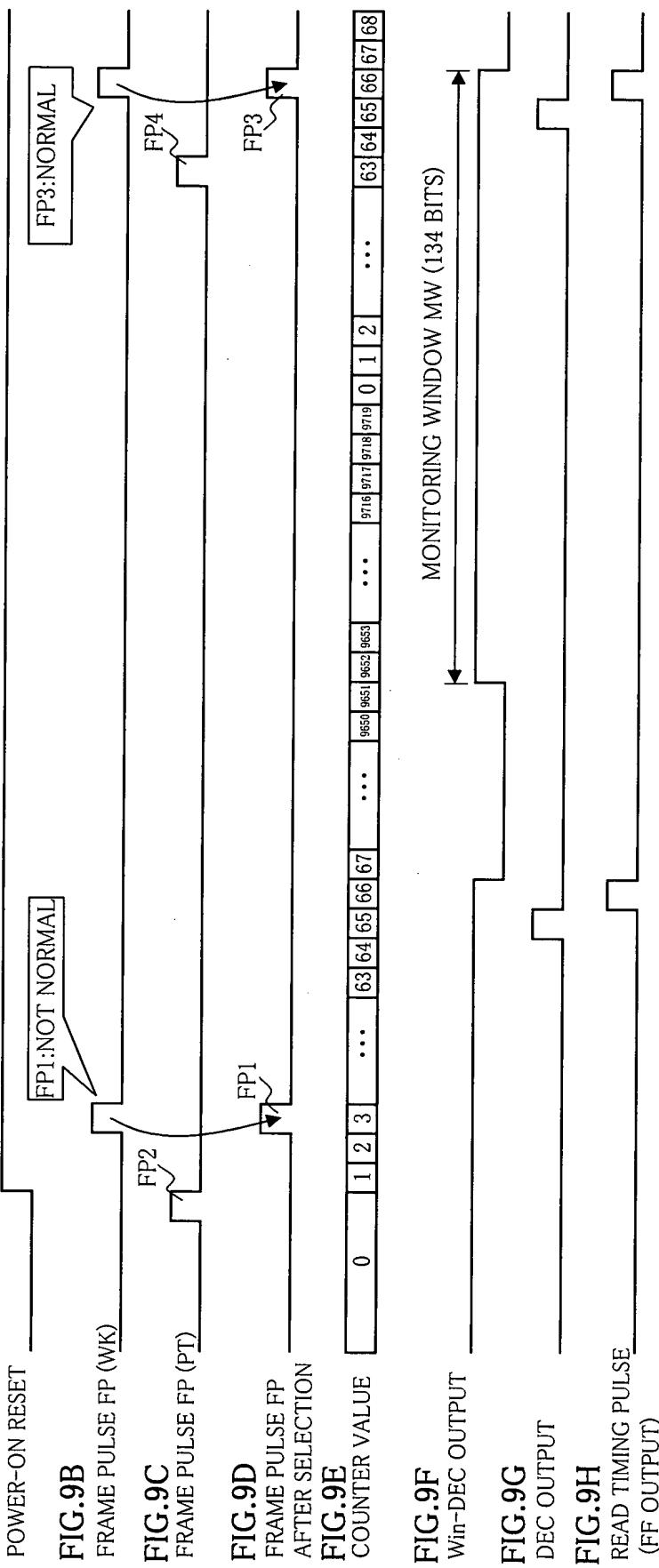
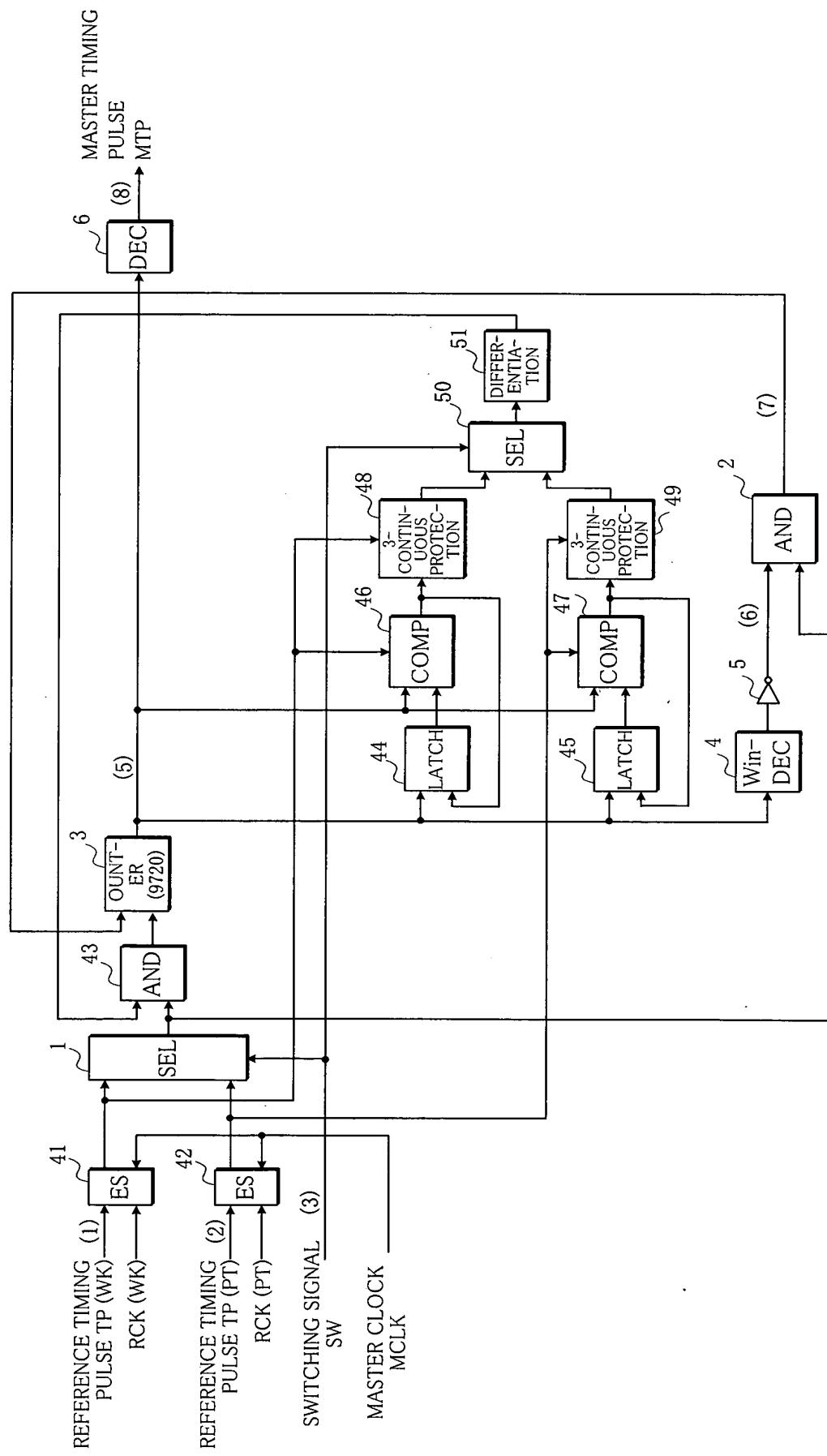
FIG.9HREAD TIMING PULSE
(FF OUTPUT)

FIG.10
PRIOR ART



PRIOR ART

